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APPLICANTS

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** CONTINUING DATA *****
 This application is a DIV of 08/784,294 01/16/1997 PAT 6,100,562 *SWC*

** FOREIGN APPLICATIONS ***** *SWC*
 JAPAN 8-088759 03/17/1996

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 1	INDEPENDENT CLAIMS 1
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Verified and Acknowledged
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TITLE
 Method of manufacturing a semiconductor device

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